

System And Method For Real Time Monitoring  
And Control Of A Computer Machine  
Environment And Configuration Profile

Inventor: Robert F. Terry

Serial No.: 09/827,451

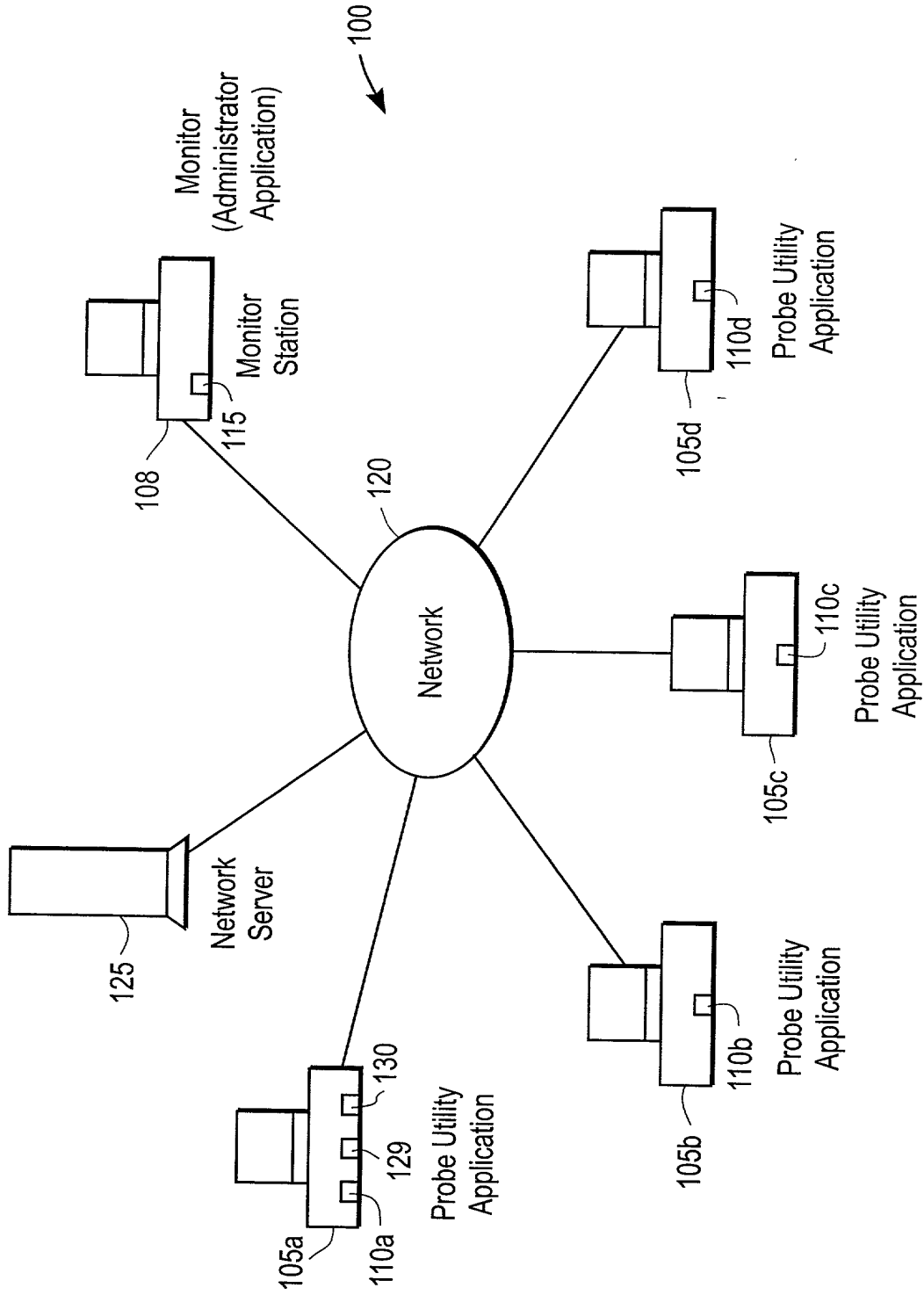


FIG. 1

FIG. 1

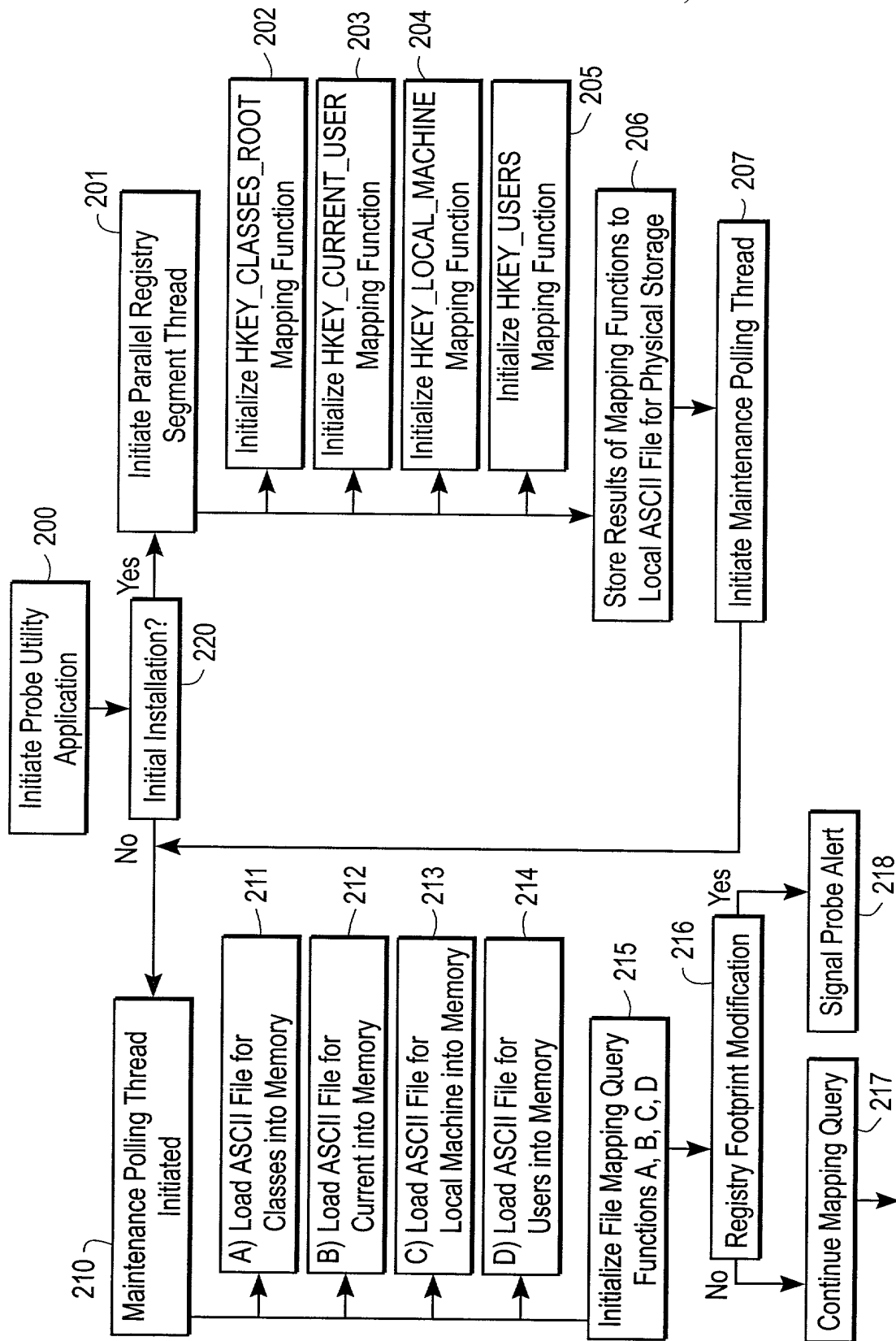


FIG. 2

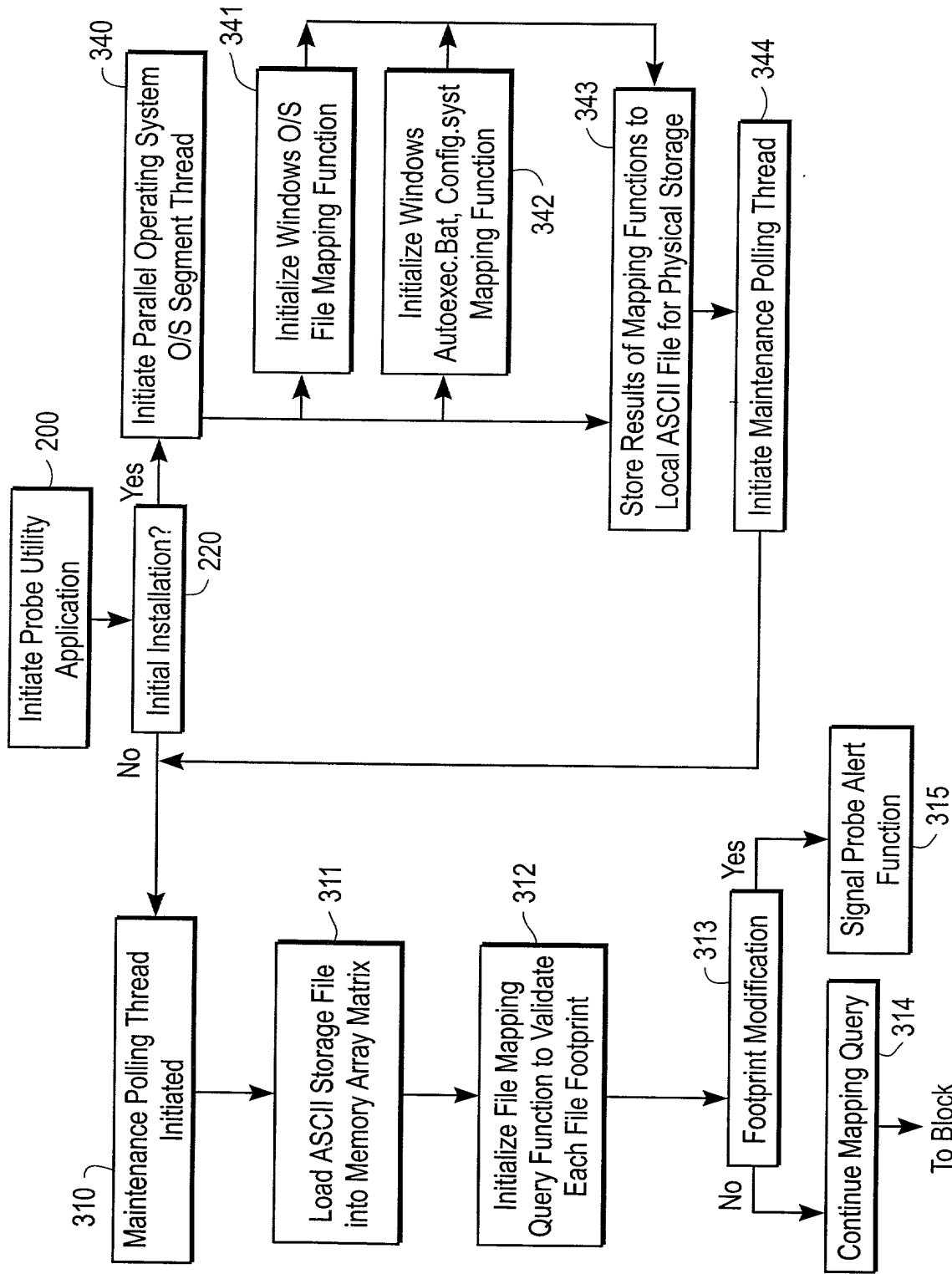


FIG. 3

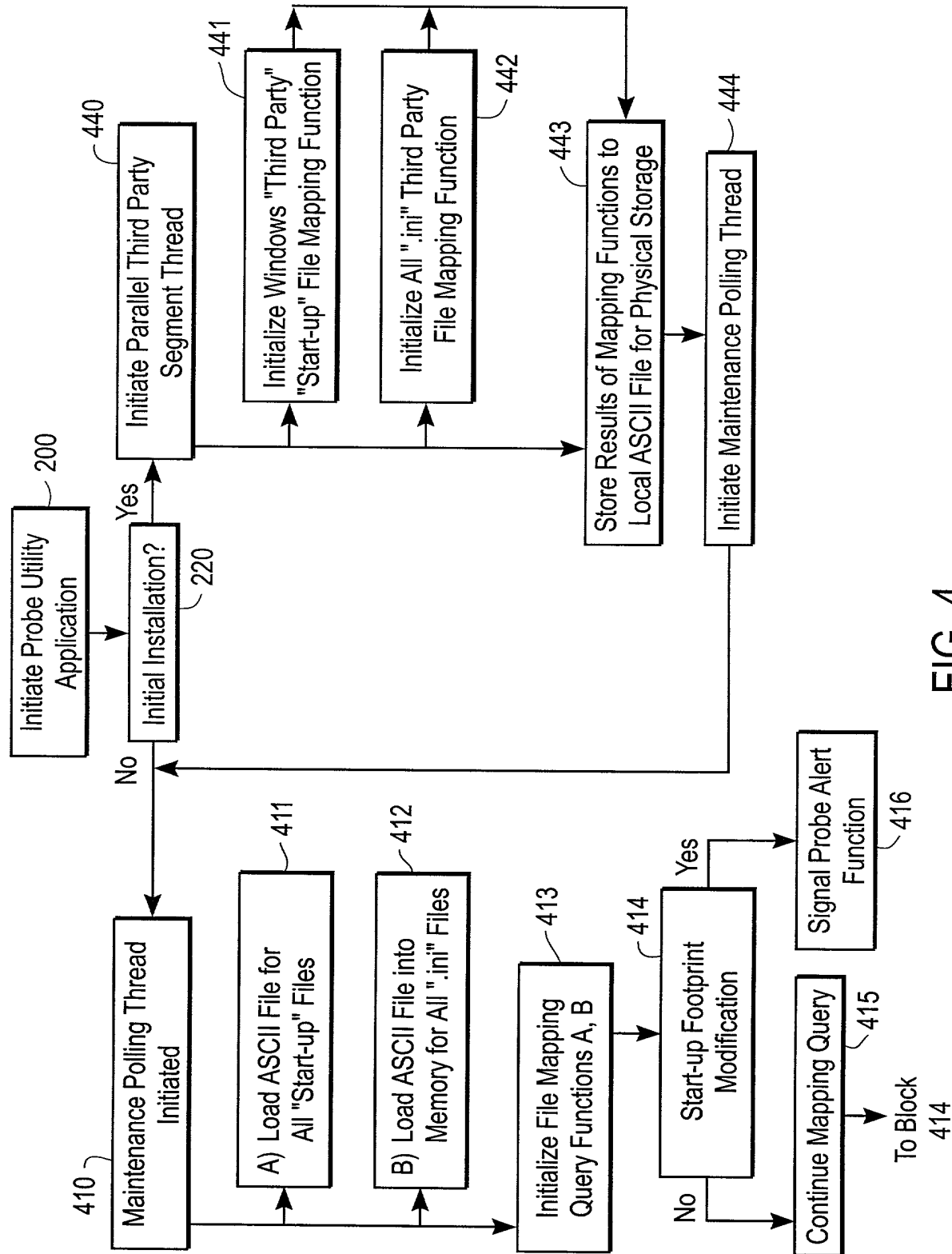


FIG. 4

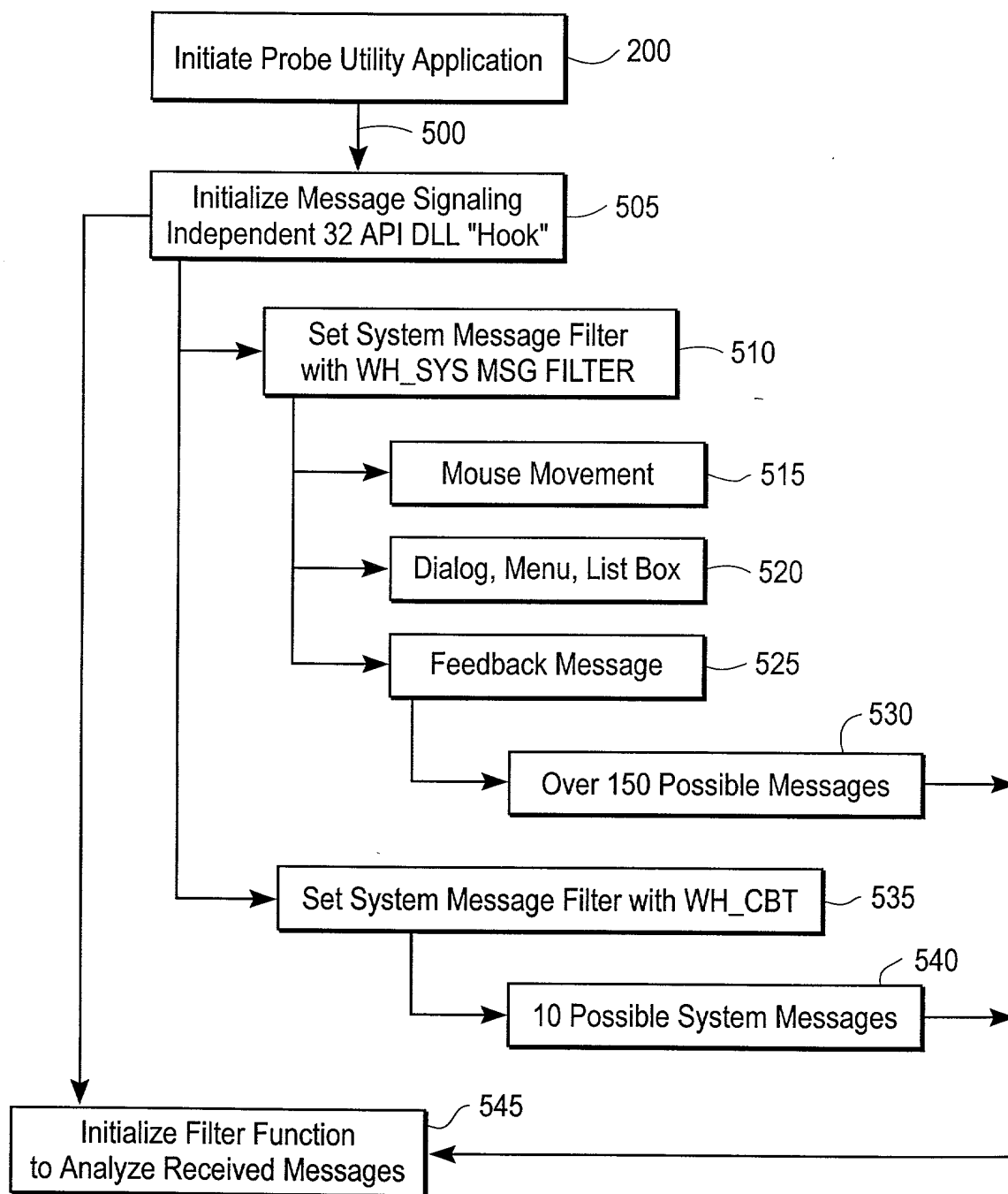


FIG. 5

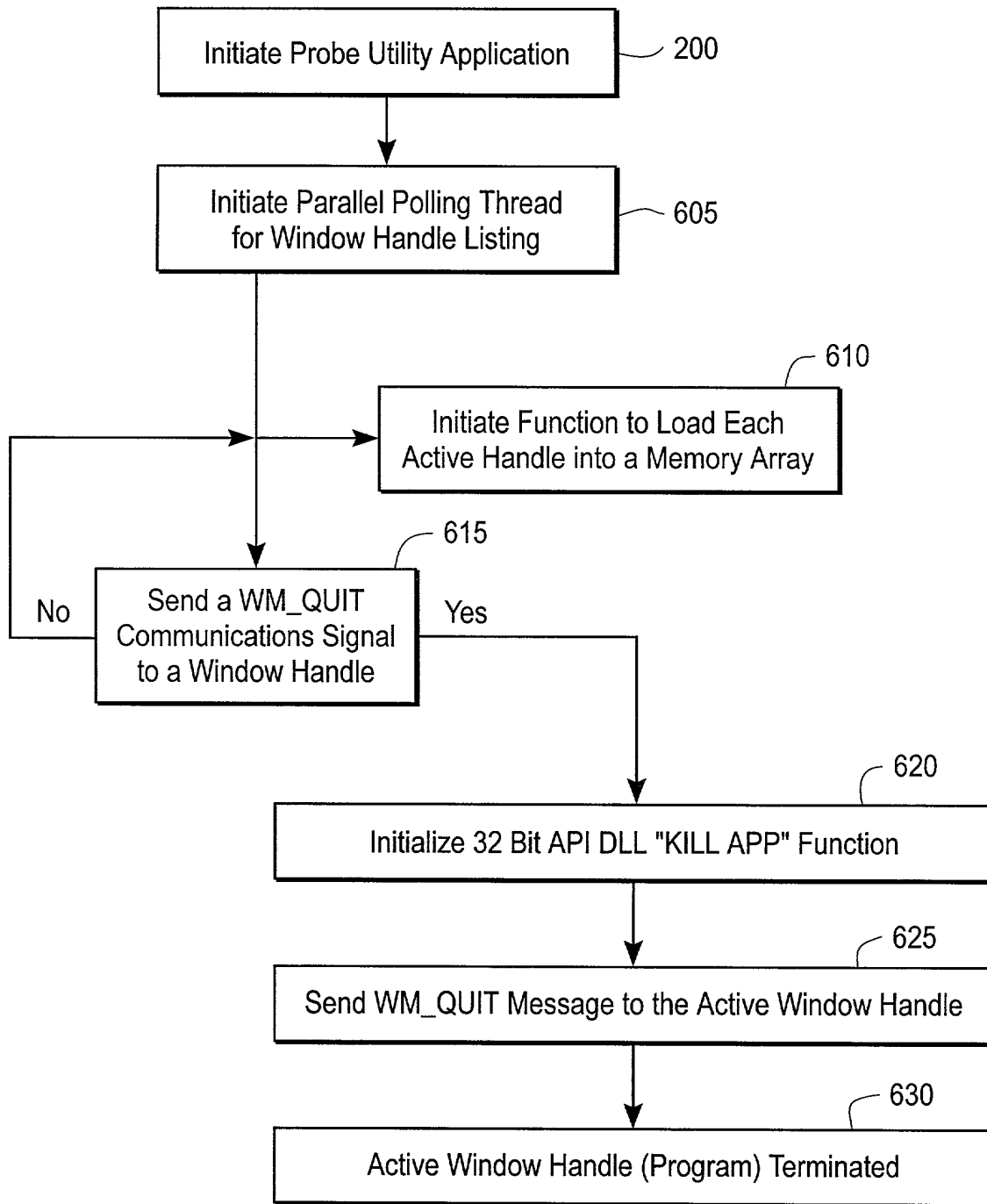


FIG. 6

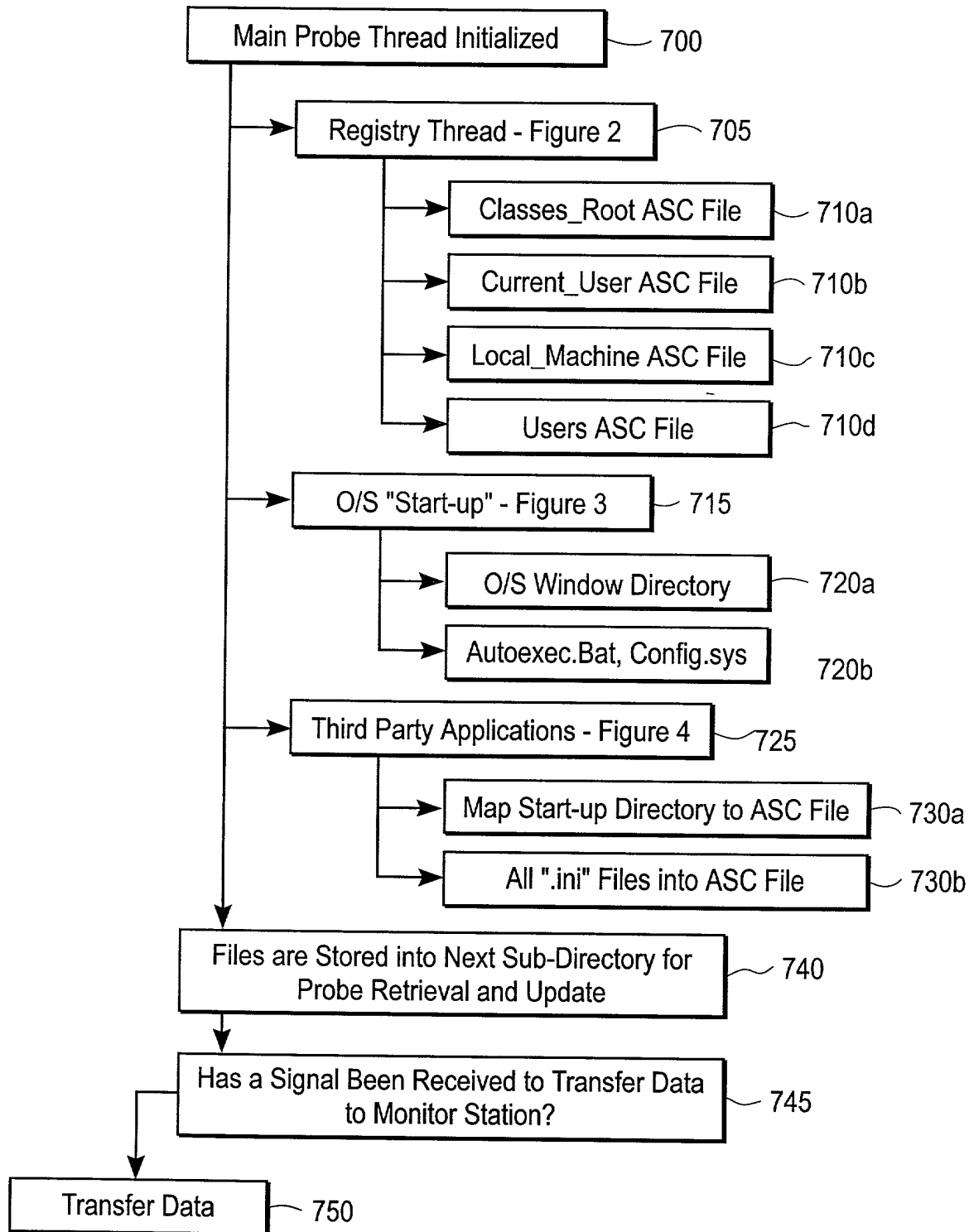


FIG. 7

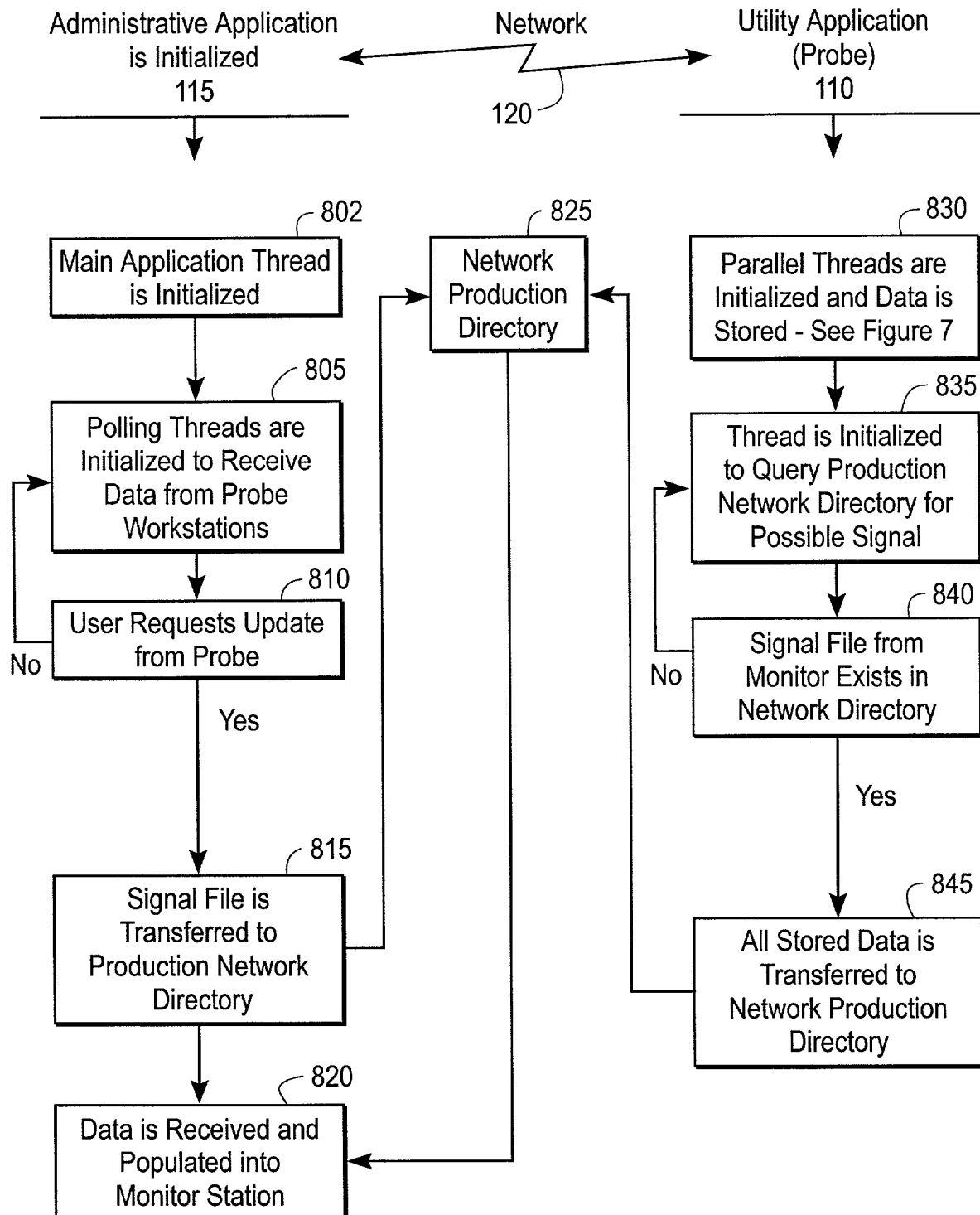


FIG. 8



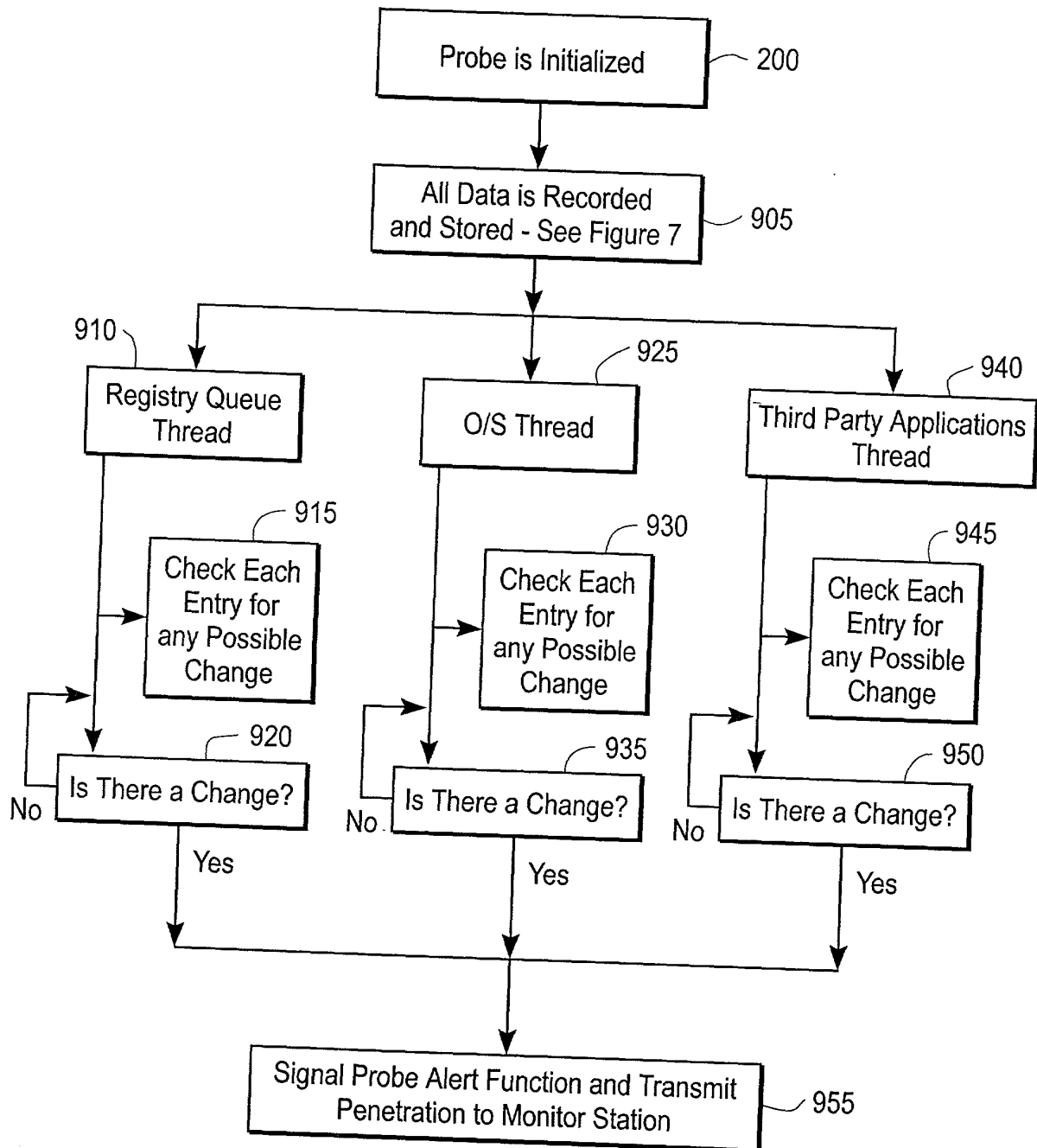


FIG. 9

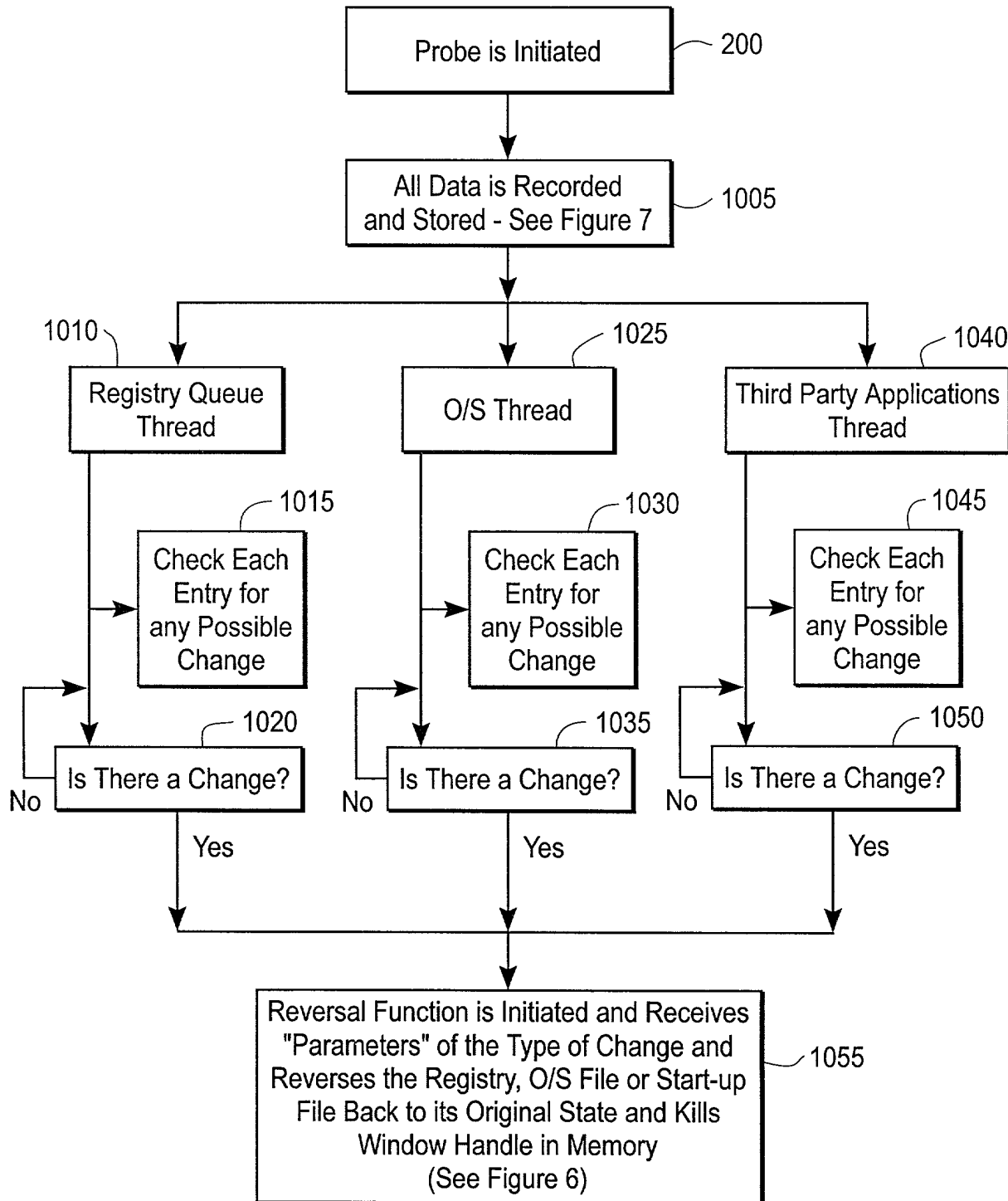
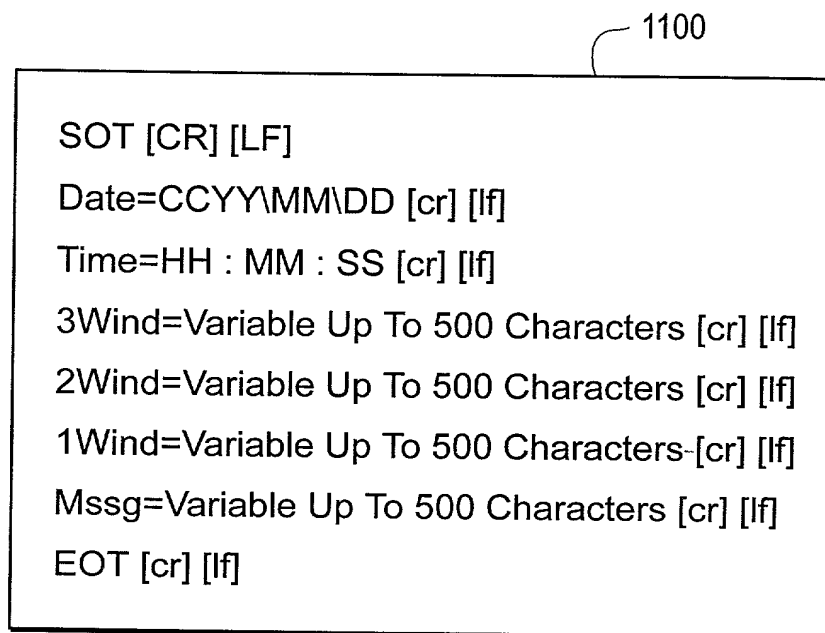


FIG. 10

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Structured Signal File Block Diagram

FIG. 11

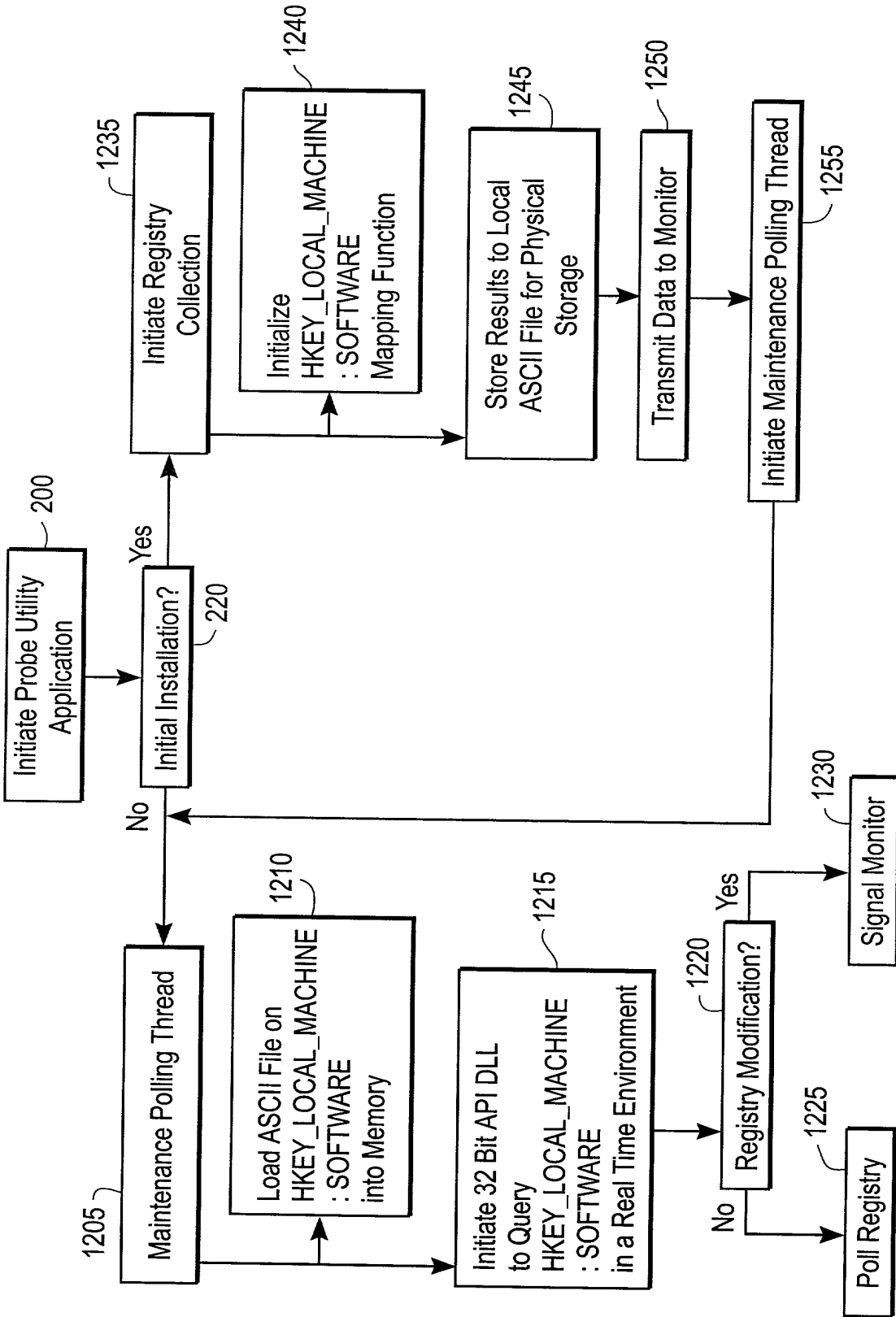


FIG. 12

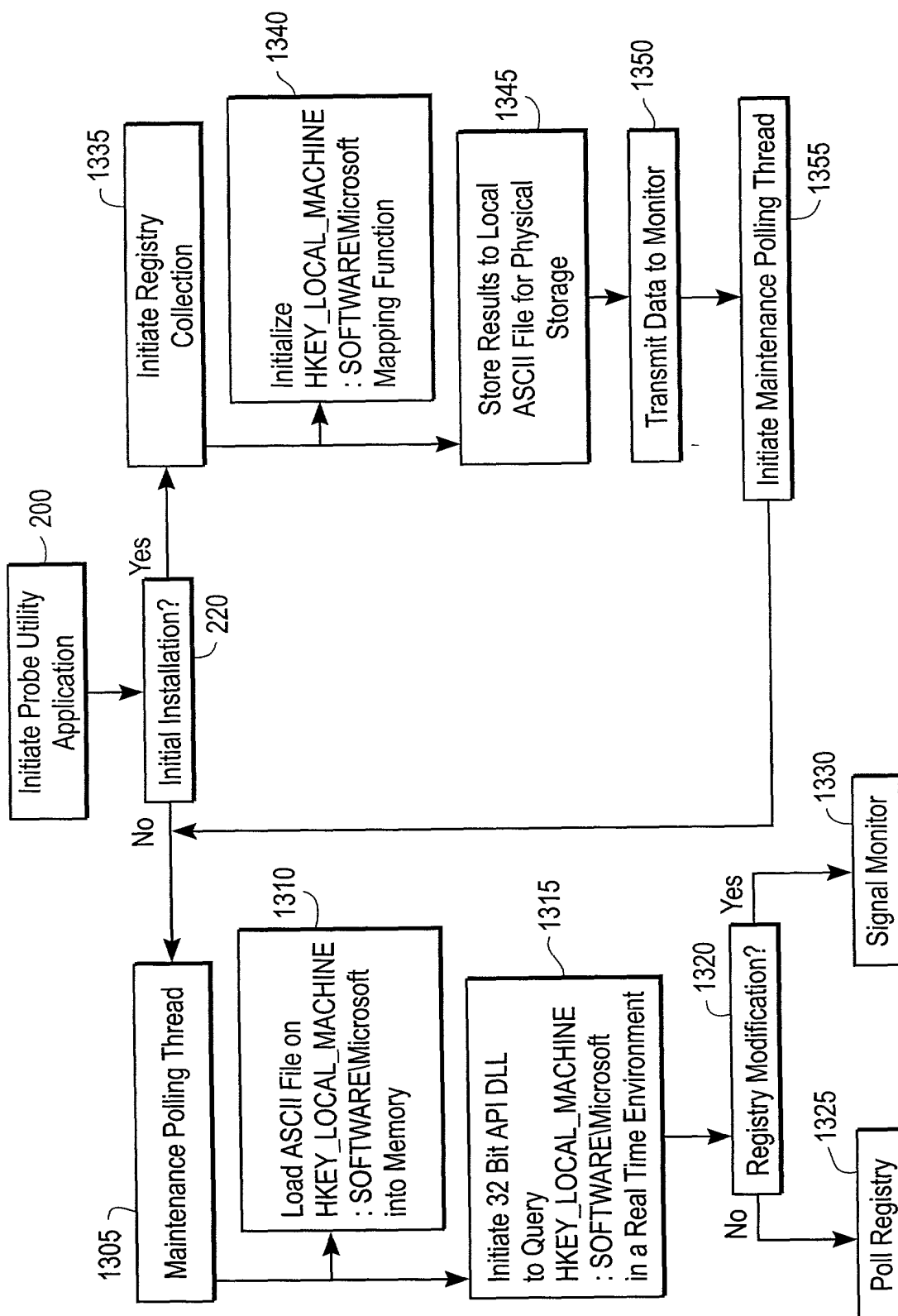


FIG. 13

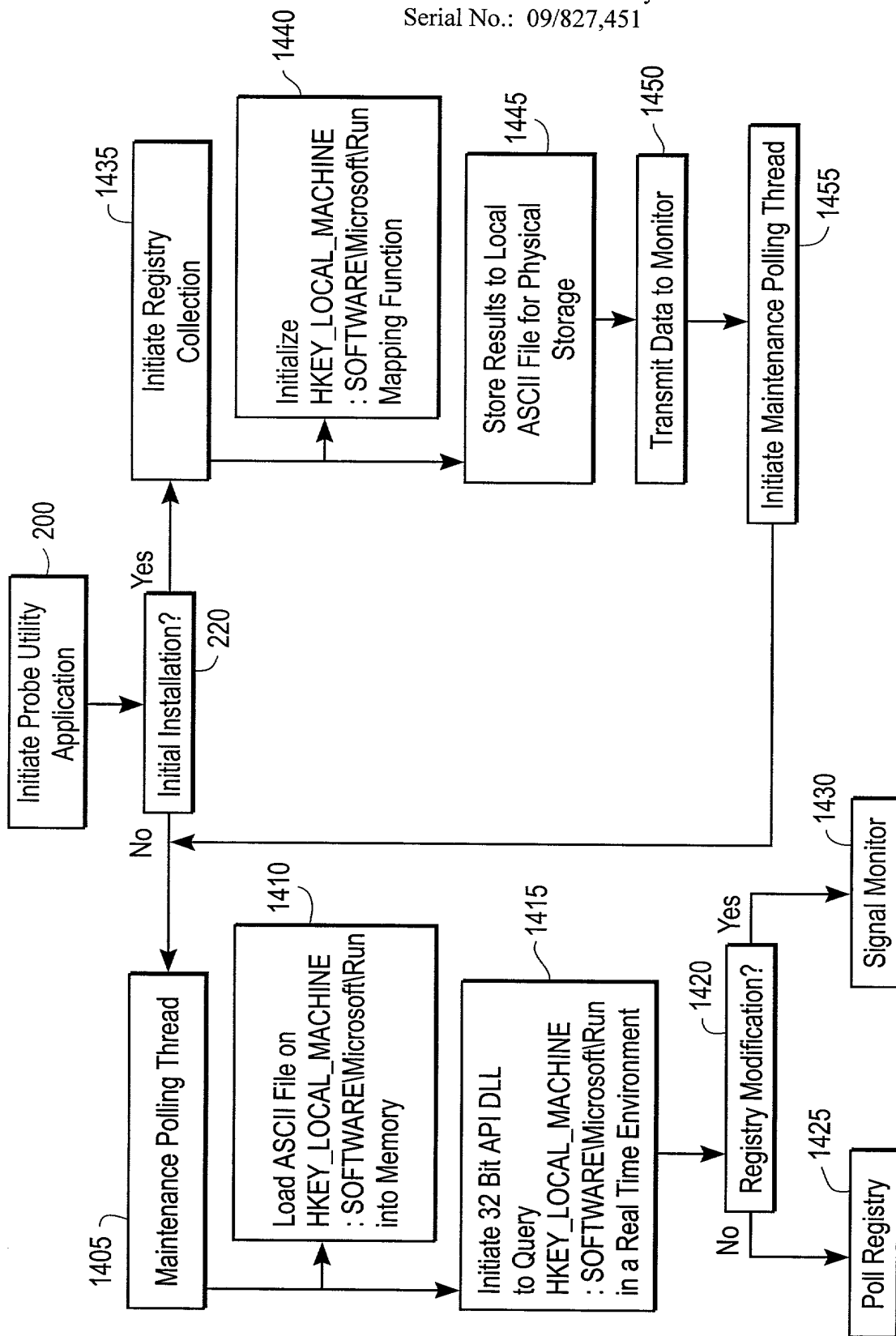


FIG. 14

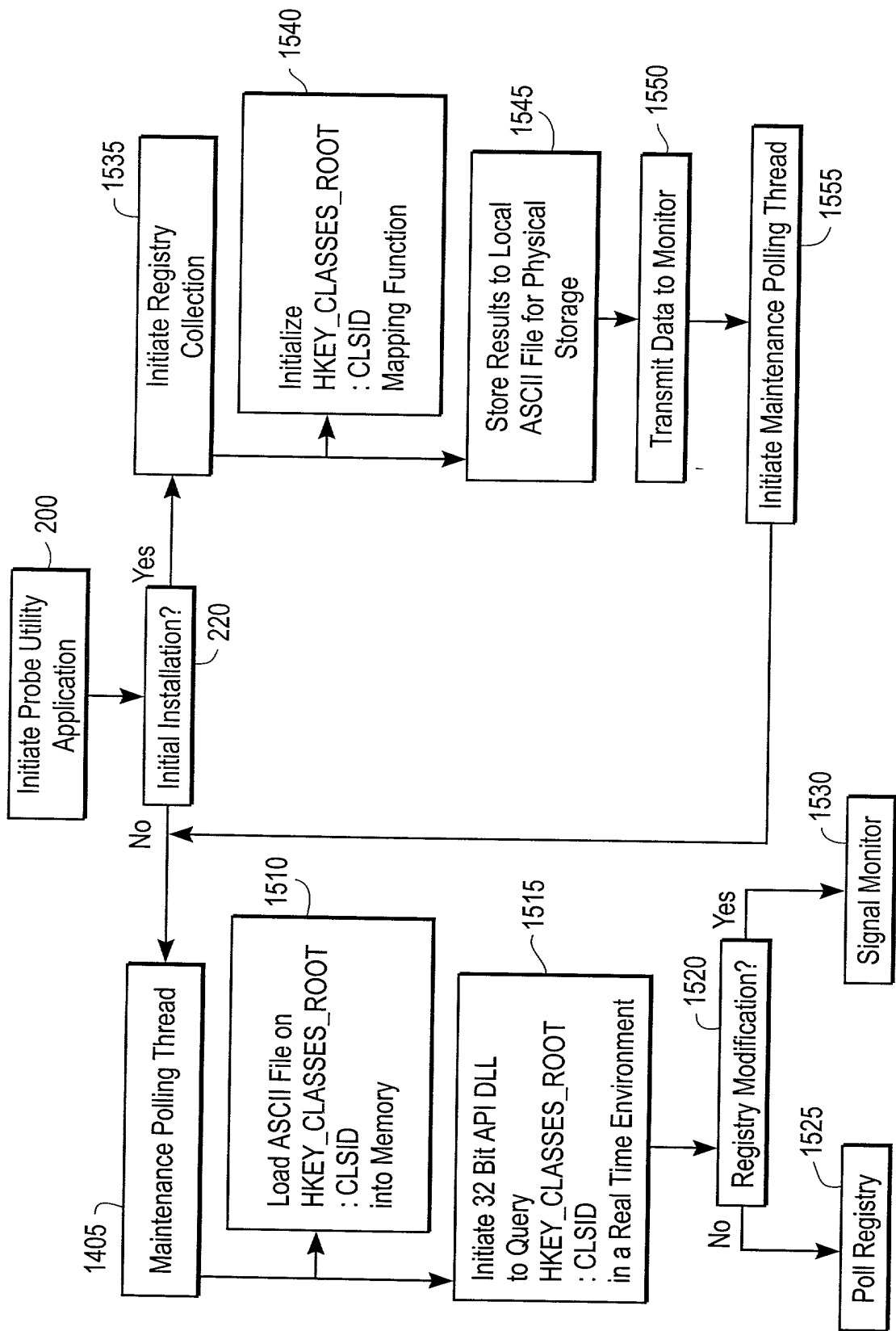


FIG. 15

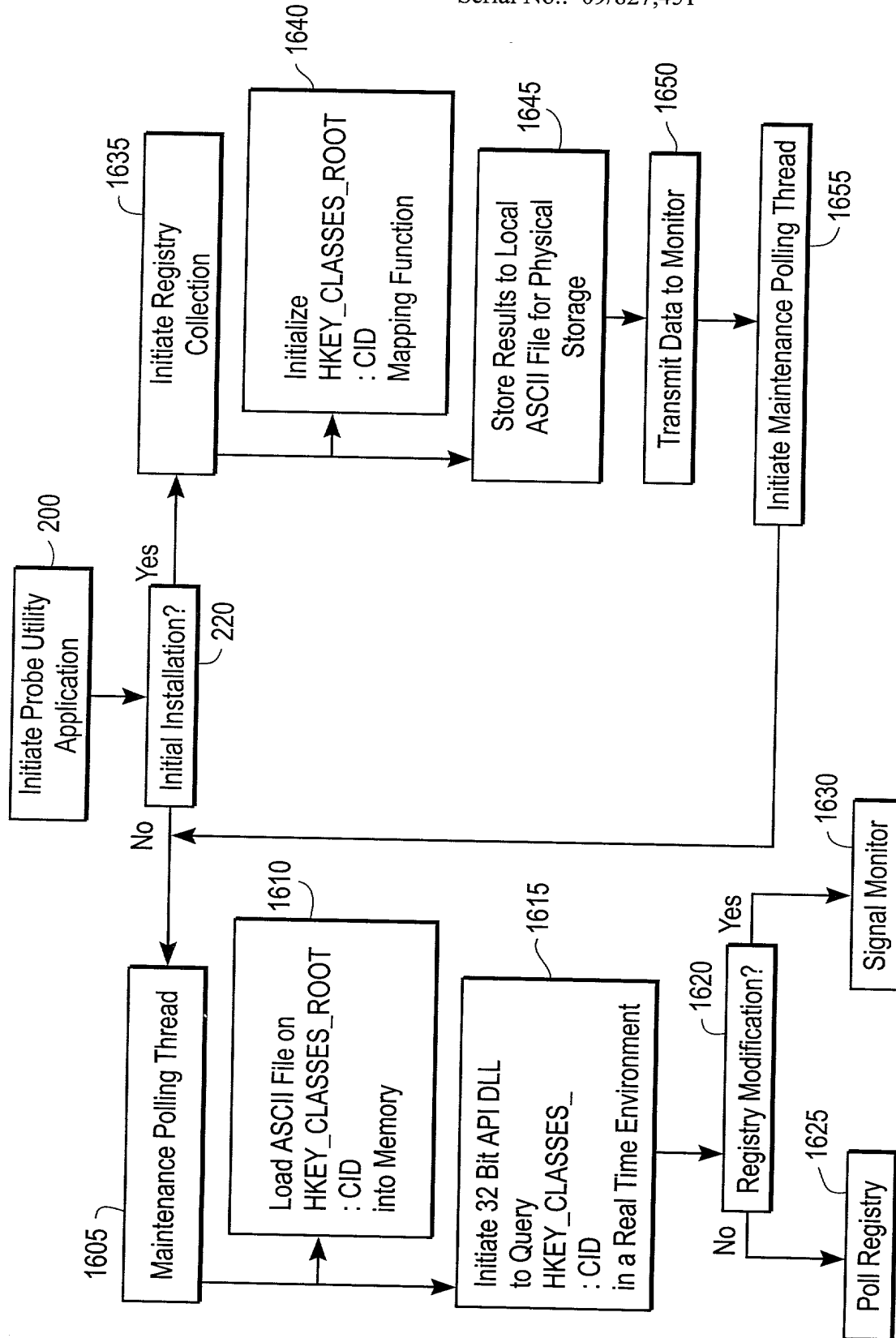


FIG. 16



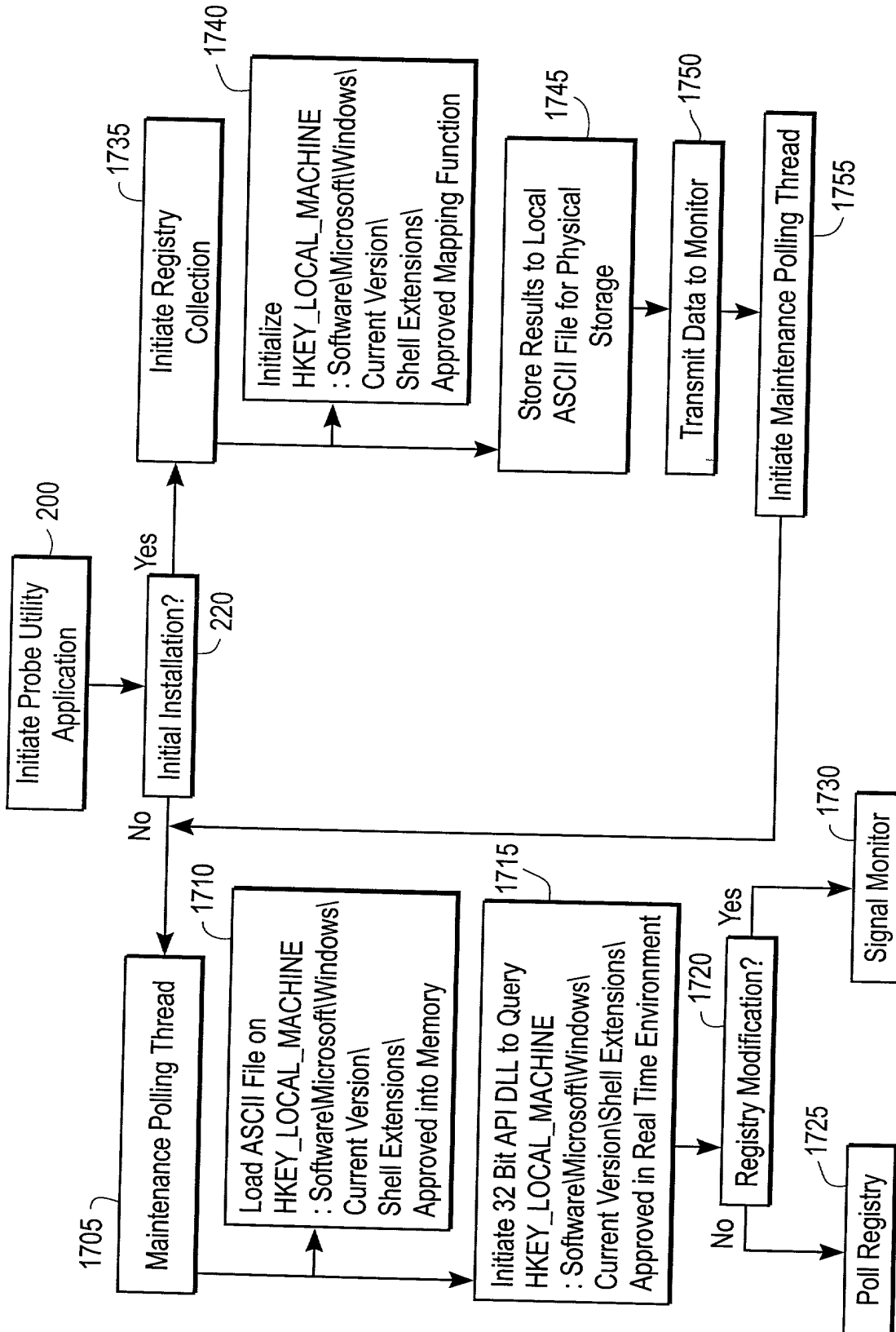


FIG. 17

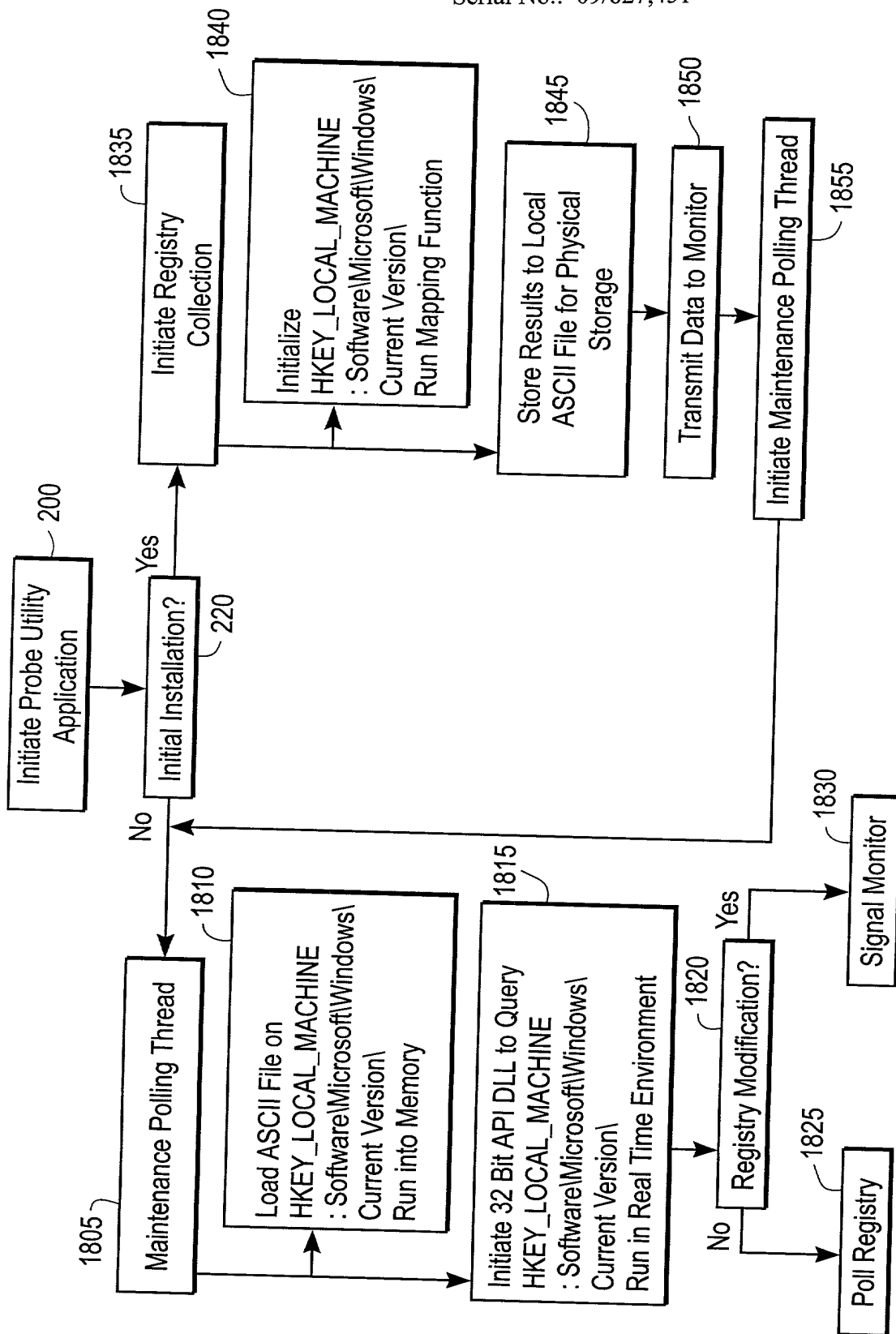


FIG. 18

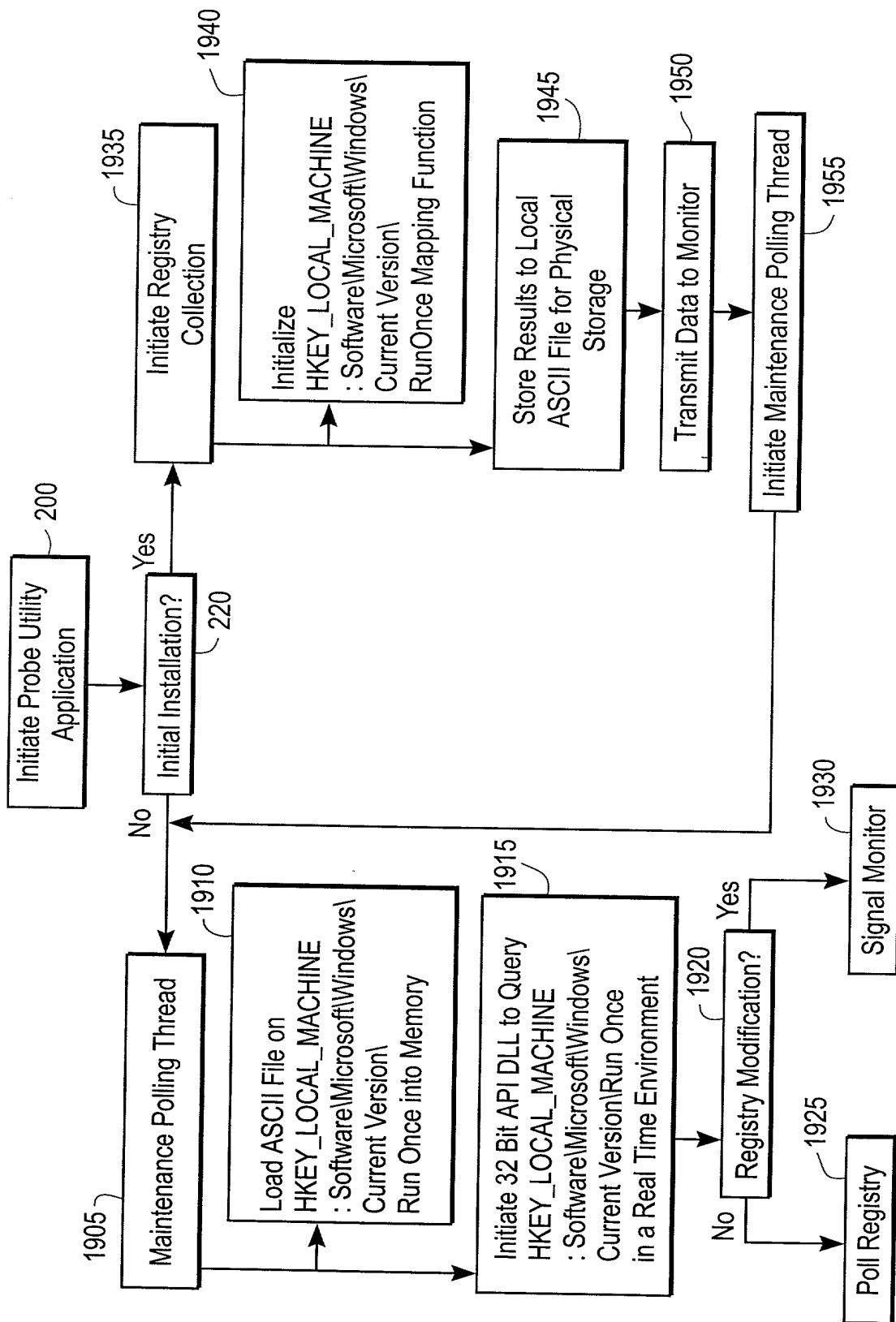


FIG. 19

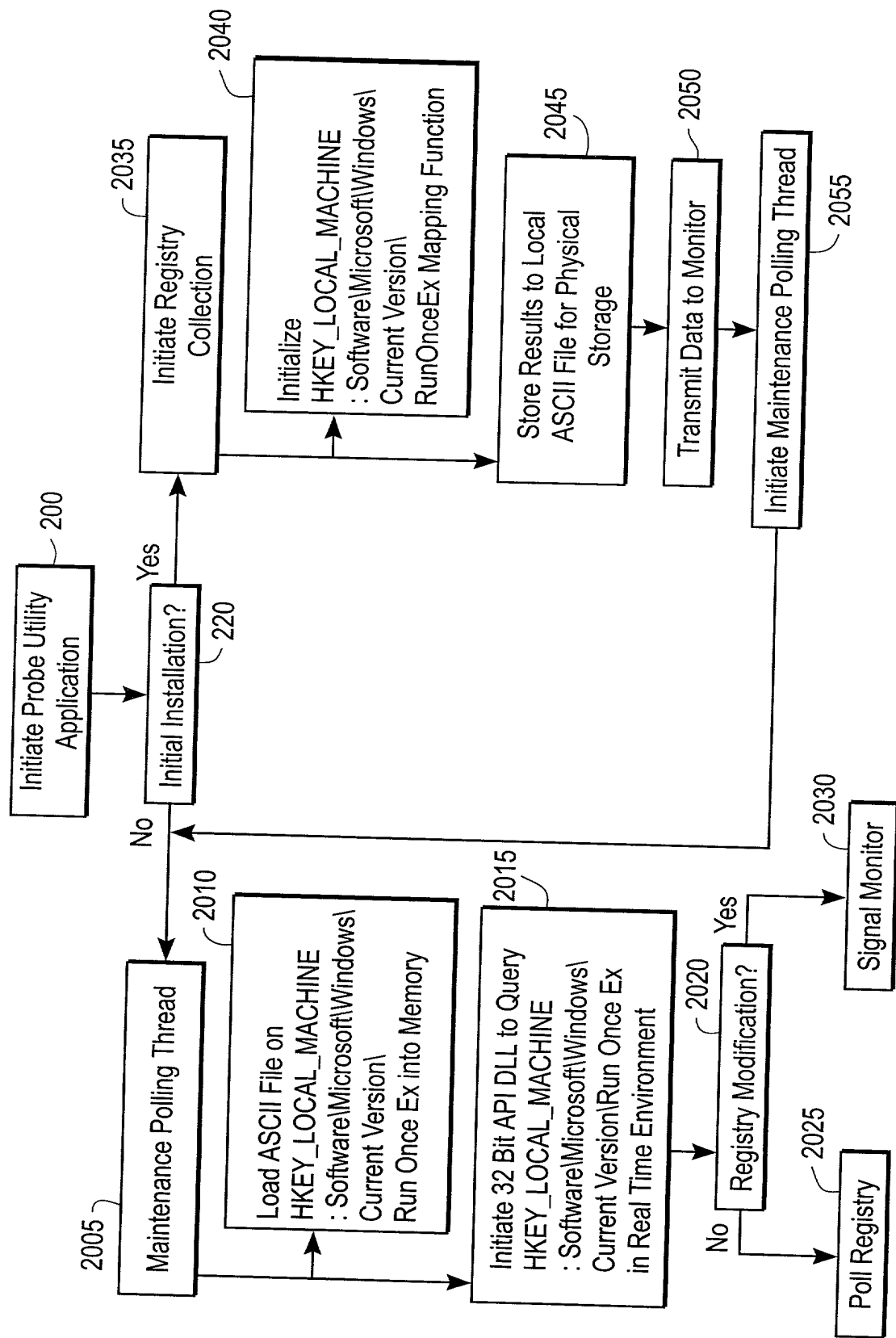


FIG. 20

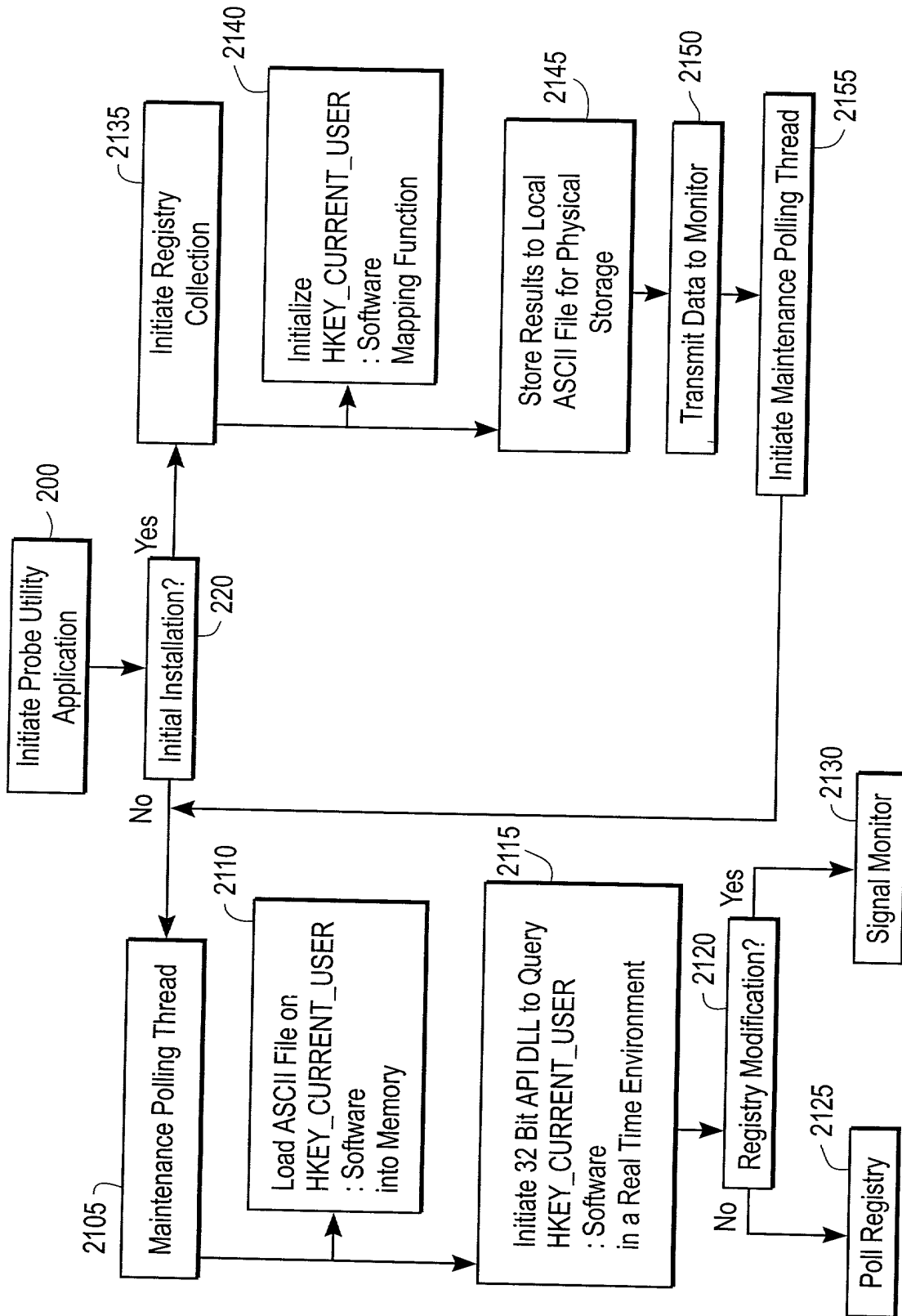
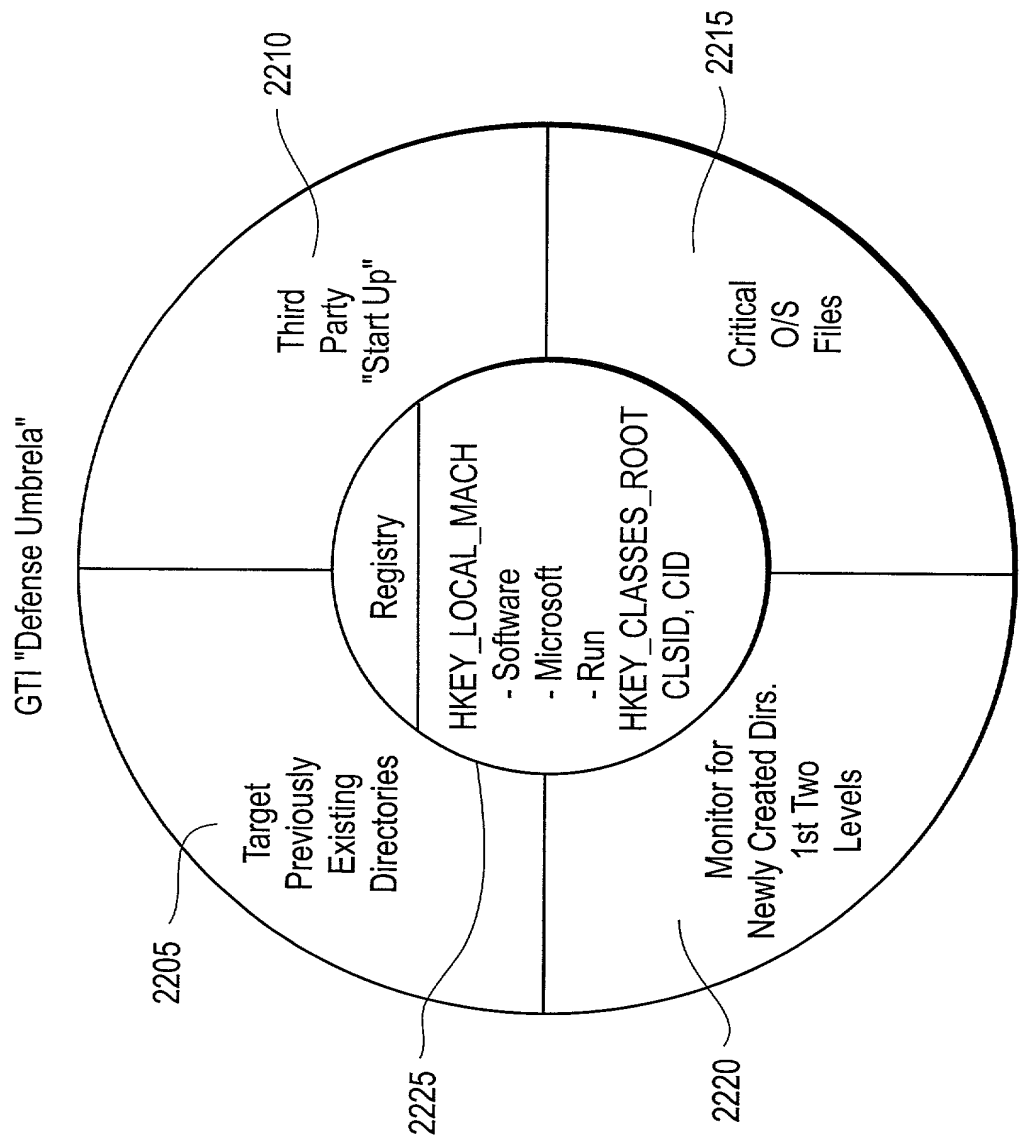


FIG. 21



"Guarded State" of the Complete PC

FIG. 22

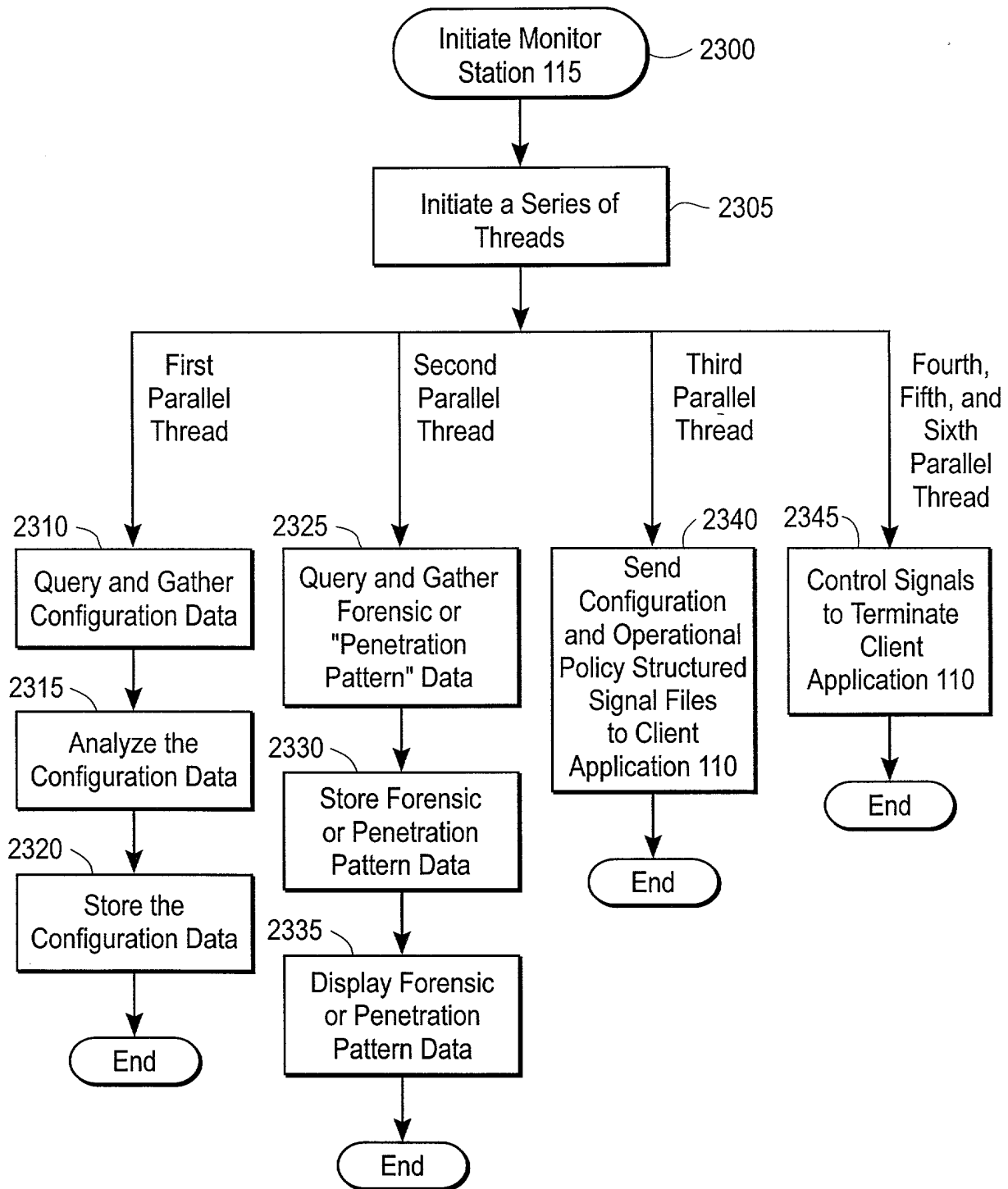


FIG. 23

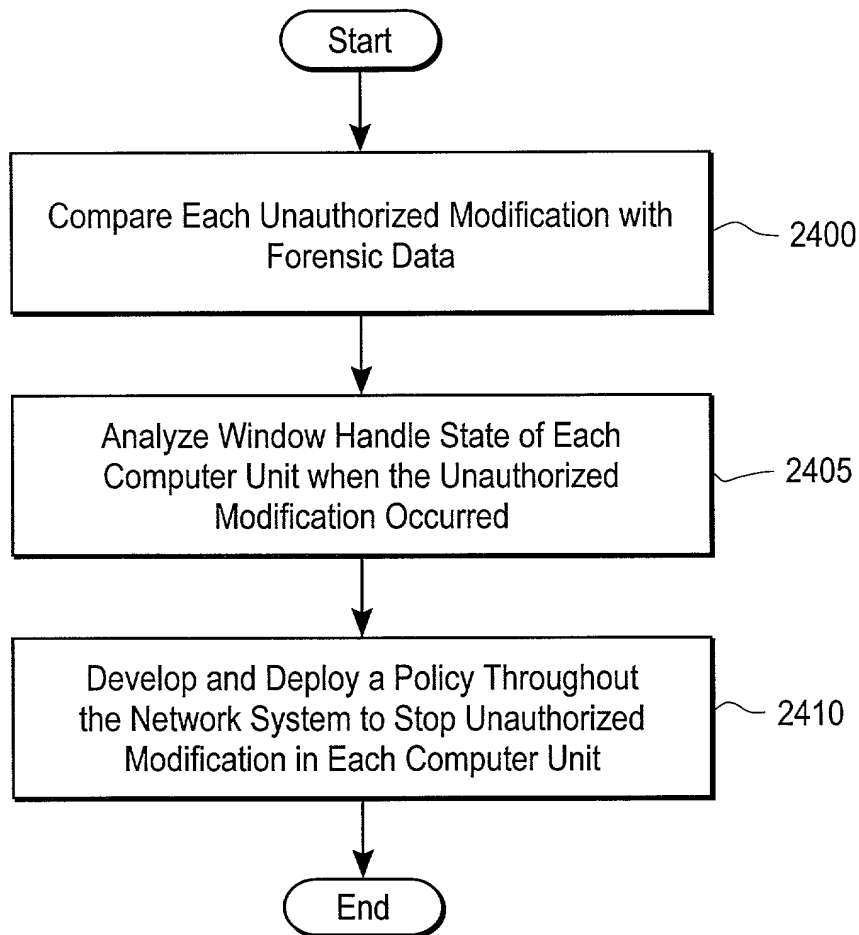


FIG. 24